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**UTILITY
PATENT APPLICATION
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

501.37465CC9

First Inventor

Y. Tanabe, et al.

Title

METHOD FOR FABRICATING
SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

Express Mail Label No.

APPLICATION ELEMENTS

SEE MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO:

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)

2. ☐ Applicant claims small entity status.
See 37 CFR 1.27.

3. ☒ Specification (Total Pages: **90**)
(preferred arrangement set forth below)

-Descriptive title of the invention
-Cross Reference to Related Applications
-Statement Regarding Fed sponsored R & D
-Reference to sequence listing, a table,
or a computer program listing appendix
-Background of the Invention
-Brief Summary of the Invention
-Brief Description of the Drawings (if filed)
-Detailed Description
-Claim(s)
-Abstract of the Disclosure

4. ☒ Drawing(s) (35 U.S.C. 113) (Total Sheets: **21**)

5. Oath or Declaration (Total Sheets: **2**)

- a. ☐ Newly executed (original or copy)
b. ☒ Copy from a prior application (37 CFR 1.63 (d))
(for continuation/divisional with Box 18 completed)

- i. ☐ **DELETION OF INVENTOR(S)**
Signed statement attached deleting inventor(s)
Named in the prior application, see 37 CFR
1.63(d)(2) and 1.33(b)

6. ☐ Application Data Sheet. See 37 CFR 1.76

7. ☐ CD-ROM or CD-R in duplicate, large table or
Computer Program (Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
a. ☐ Computer Readable Form (CRF)
b. Specification Sequence Listing on:
i. ☐ CD-ROM or CD-R (2 copies); or
ii. ☐ paper
c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

9. ☐ Assignment Papers (cover sheet & documents(s))
10. ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney
(when there is an assignee)
11. ☐ English Translation Document (if applicable)
12. ☒ Information Disclosure ☐ Copies of IDS Citations
Statement (IDS)/PTO-1449
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
16. ☐ Nonpublication Request under 35 U.S.C. 122
(b)(2)(B)(i). Applicant must attach form PTO/SB/35
or its equivalent.
17. ☒ Other: **Claim For Priority, Terminal Disclaimer and
Credit Card Payment Form**

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment,
or in an Application Data Sheet under 37 CFR 1.76:

☒ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: **10/424,105**

Prior application information: Examiner: **L. Kilday** Art Unit: **2829**

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under
Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference.
The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

19. CORRESPONDENCE ADDRESS☒ Customer Number

020457

OR ☐ Correspondence address below

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ANTONELLI, TERRY, STOUT & KRAUS

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Name (Print/Type)

William I. Solomon

Registration No. (Attorney/Agent)

28,565

Signature

Date

February 10, 2004

This collection of information is required by 37 CFR 1.53(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**FEE TRANSMITTAL
for FY 2004**

Effective 10/01/2003. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27**TOTAL AMOUNT OF PAYMENT** (\$)**1,114.00****Complete if Known**

Application Number	TBD Cont. f 10/424,105
Filing Date	February 10, 2004
First Named Inventor	Y. Tanabe, et al.
Examiner Name	L. Kilday
Art Unit	2829
Attorney Docket No.	501.37465CC9

METHOD OF PAYMENT (check all that apply)☐ Check ☒ Credit Card ☐ Money Order ☐ Other ☐ None☒ Deposit Account:Deposit
Account
Number
Deposit
Account
Name

01-2135

Antonelli, Terry, Stout & Kraus, LLP

The Director is authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☒ Credit any overpayments☒ Charge any deficiencies.☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account.**FEE CALCULATION****1. BASIC FILING FEE**

Large Entity Code	Large Entity Fee (\$)	Small Entity Code	Small Entity Fee (\$)	Fee Description	Fee Paid
1001	770	2001	385	Utility filing fee	770.00
1002	340	2002	170	Design filing fee	
1003	530	2003	265	Plant filing fee	
1004	770	2004	385	Reissue filing fee	
1005	160	2005	80	Provisional filing fee	

SUBTOTAL (1)

770.00

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

	Extra Claims	Fee from below	Fee Paid
Total Claims	33	-20** = 13 x 18.00	= 234.00
Indep. Claims	1	-3** = 0 x 86.00	= 0.00
Multiple Dependent		290.00	= 0.00

Large Entity Small Entity

Large Entity Code	Large Entity Fee (\$)	Small Entity Code	Small Entity Fee (\$)	Fee Description
1202	18	2202	9	Claims in excess of 20
1201	86	2201	43	Independent claims in excess of 3
1203	290	2203	145	Multiple dependent claim, if not paid
1204	86	2204	43	** Reissue independent claims over original patent
1205	18	2205	9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) \$

1,004.00

**or number previously paid, if greater; For Reissues, see above.

FEE CALCULATION (continued)**3. ADDITIONAL FEES****Large Entity Small Entity**

Large Entity Code	Large Entity Fee (\$)	Small Entity Code	Small Entity Fee (\$)	Fee Description
1051	130	2051	65	Surcharge - late filing fee or oath
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet
1053	130	1053	130	Non-English specification
1812	2,520	1812	2,520	For filing a request for ex parte reexamination
1804	920*	1804	920*	Requesting publication of SIR prior to Examination action
1805	1,840*	1805	1,840*	Requesting publication of SIR after Examiner action
1251	110	2251	55	Extension for reply within first month
1252	420	2252	210	Extension for reply within second month
1253	950	2253	475	Extension for reply within third month
1254	1,480	2254	740	Extension for reply within fourth month
1255	2,010	2255	1,005	Extension for reply within fifth month
1401	330	2401	165	Notice of Appeal
1402	330	2402	165	Filing a brief in support of an appeal
1403	290	2403	145	Request for oral hearing
1451	1,510	1451	1,510	Petition to institute a public use proceeding
1452	110	2452	55	Petition to revive - unavoidable
1453	1,330	2453	665	Petition to revive - unintentional
1501	1,330	2501	665	Utility issue fee (or reissue)
1502	480	2502	240	Design issue fee
1503	640	2503	320	Plant issue fee
1406	130	1460	130	Petitions to the Commissioner
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)
1806	180	1806	180	Submission of Information Disclosure Stmt
8021	40	8021	40	Recording each patent assignment per property (times number of properties)
1809	770	2809	385	Filing a submission after final rejection (37 CFR § 1.129(a))
1810	770	2810	385	For each additional invention to be examined (37 CFR § 1.129(b))
1801	770	2801	385	Request for Continued Examination (RCE)
1802	900	1802	900	Request for expedited examination of a design application

Fee Paid

Other fee (specify) **Terminal Disclaimer**


110.00

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$)

1,114.00

SUBMITTED BY**Complete (if applicable)**

Name (Print/Type)	William I. Solomon	Registration No. (Attorney/Agent)	28,565	Telephone	703-312-6600
Signature				Date	02/10/2004

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): TANABE, et al.
Application No. TBD
Filed: February 10, 2004
For: METHOD FOR FABRICATING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Expected 2829
Group:
Expected L. Kilday
Examiner:

TERMINAL DISCLAIMER

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 10, 2004

Sir:

Petitioner, Hitachi, Ltd., having its place of business at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo 100, Japan, represents that it is the sole owner of the entire interest of the above-identified U.S. patent application, filed on even date herewith, under Attorney Docket No. 501.37465CC9, for METHOD FOR FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, and that the Assignment of all rights in connection therewith has been recorded at Reel 010361, Frame 0584.

Petitioner hereby disclaims all that portion of the term of any patent to be issued on the above-identified application subsequent to the expiration date of the full statutory term, defined in 35 U.S.C. §154 to §156 and §173, of U.S. Patent

No. 6,528,431, issued March 4, 2003, and hereby agrees that any patent issued on the above-identified application shall be enforceable only for and during such time as the said U.S. Patent No. 6,528,431 and the above-identified application are commonly owned.

Petitioner, however, does not disclaim the terminal part of any patent granted on the instant application prior to the expiration date of the full statutory term, defined in 35 U.S.C. § 154 to § 156 and § 173, of the above-listed U.S. Patent No. 6,528,431 in the event that U.S. Patent No. 6,528,431 expires: for failure to pay a maintenance fee; is held unenforceable; is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. §1.321; has all claims cancelled by a reexamination certificate; is reissued; or is otherwise terminated prior to the expiration of the above-referred-to full statutory term, except for the separation of legal title as stated above.

This disclaimer is to be binding with respect to any patent granted on the above-identified application, and is binding upon grantees, their successors, or assignees of any interests.

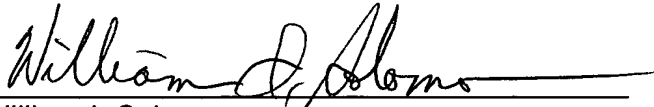
The undersigned is an attorney of record in this application and is empowered to act on behalf of Hitachi, Ltd. for execution and submission of Terminal Disclaimers, in accordance with the provisions of 37 C.F.R. §1.321(b) and (c), effective January 4, 1994.

The undersigned hereby declares that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the

knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



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